

# EureCard DOMINO Gamma

*Image Acquisition from High-End Analog Cameras*



The Domino Gamma is the ideal solution for **high-end multiple-camera applications**.

The **Domino Gamma** provides acquisition from typically **three analog cameras** (up to 6) on a single PCI board. The **Domino Gamma** is fitted with **3 connectors** and **four digitizers**. Simultaneous acquisition from all channels is possible. Three timing generators, three trigger and strobe lines are available for **independent camera control**.

The Domino Gamma supports top-notch high-resolution cameras with a pixel clock frequency up to **40 MHz**. It is compatible with single-tap, dual-tap and RGB cameras and generates 8-bit or 10-bit pixels per channel. Asynchronous reset, exposure control and progressive scanning are supported.

It comes with the **MultiCam** driver, which drastically simplifies its integration into your machine vision application.

## Support of monochrome (single or dual-tap) & RGB analog cameras

- Four 10-bit 40 MHz A/D converters
- High-resolution, typically 1300 X 1030
- Independent camera control:  
3 timing generators, 3 trigger & strobe lines
- Asynchronous reset, shutter control and progressive-scan support
- 16 Mbyte frame buffer

Libraries,  
DLLs and  
ActiveX Controls  
Included



[www.euresys.com](http://www.euresys.com)  
[info@euresys.com](mailto:info@euresys.com)

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**EURESYS**  
Excellence  
in vision

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## M A I N F E A T U R E S

### CONNECTIONS

#### Camera connection

Three DB15HD connectors on the main bracket including:

- Differential / single ended video inputs (75 ohms)
- Reset, VD, HD, exposure control and pixel clock lines
- Specific connection for 12 V camera supply

#### I/O connections

- RS-232 (default) or RS-485 (optional) DB9 connector on the extra bracket.
- Trigger-strobe (3 lines each) DB9 connector on the extra bracket.
- General purpose I/O DB9 connector on the extra bracket.



### ACQUISITION

- Four 10-bit 40 MHz **A/D converters**.
- Four 10-bit input **look-up-tables**.
- Support of automatic precise **input calibration**.
- 20, 10 or 5 MHz programmable **input filter**.
- Programmable **gain and offset** control.

### TIMING GENERATORS

Three independent **timing generators**.

### CAMERA SUPPORT

Support of **single-tap**, **dual-tap** and **RGB analog** cameras.  
Support of **high-resolution** cameras, typically 1300 x 1030.

**Interlaced or progressive scanning**.

**Asynchronous reset and shutter control**.

Camera **pixel clock** support.

**Typical configurations supported:**

- Three independent single-tap cameras
- Two independent dual-tap cameras
- Two independent single-tap and one dual-tap cameras
- One RGB camera and one monochrome single-tap camera

### FRAME BUFFER

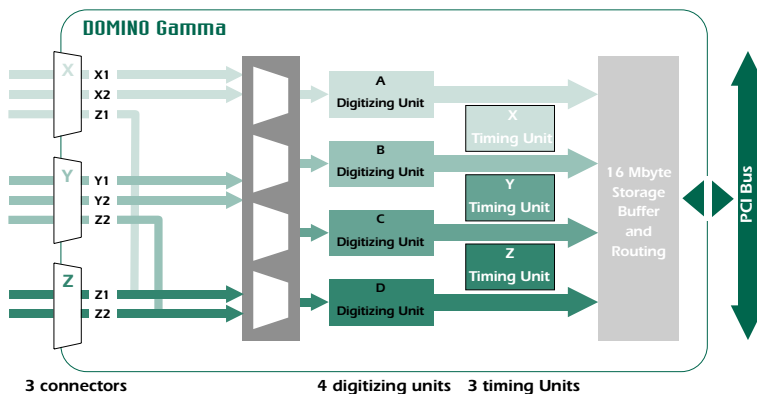
**16 Mbyte SDRAM** frame buffer.

### PCI INTERFACE

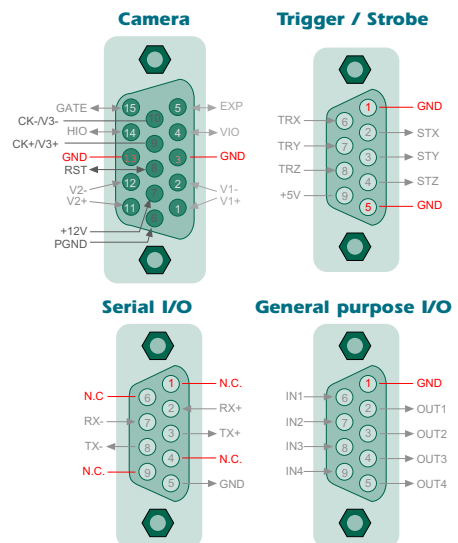
PCI rev 2.2 (5 V and 3.3 V) compliant interface.

Support of DMA and bus mastering.

### BLOCK DIAGRAM



### CONNECTORS



#### Euresys

14, avenue du Pré Aily, B-4031 Angleur, BELGIUM  
Phone: +32 (4) 367 72 88 Fax: +32 (4) 367 74 66

#### US Office

US Toll free: 800 995 8009 Phone: 619 444 8300

E-mail: [info@euresys.com](mailto:info@euresys.com) Web site: [www.euresys.com](http://www.euresys.com)



#### Your distributor

Opto Vision France - Z.I. Thibaud,  
9 rue Boudeville, 31104 Toulouse cedex 1  
Tél. 05 61 76 51 11 - Fax 05 61 76 51 52